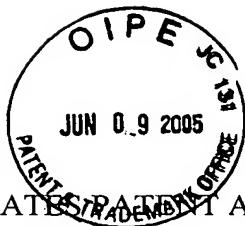


Attorney Docket No. 5649-1165



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Chung et al.

Serial No.: 10/702,366

Filed: November 6, 2003

For: METHODS FOR GENERATING OUTPUT CONTROL SIGNALS IN
SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICES AND RELATED
SEMICONDUCTOR MEMORY DEVICES

Examiner: David Lam

Group Art Unit: 2827

Confirmation No. 8507

Date: June 7, 2005

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**NOTIFICATION REGARDING EXAMINER'S AMENDMENT AND
COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Sir:

Applicants appreciate the issuance of the Notice of Allowability in this case.

Applicants are filing the present paper to address two informalities in the Examiner's Amendment and Statement of Reasons for Allowance which accompanied the Notice of Allowability.

First, the Examiner's Amendment states that "Figures 1-2 have been labeled as 'Prior Art'. *See attachment.*" Applicants agree that Figures 1-2 should be labeled as prior art, and have no objection to such an Amendment to the drawings. Applicants note, however, that the cited attachment appears to inadvertently have not been provided with the Office Action. As Applicants assume that the attachment merely was copies of Figures 1-2 with the "Prior Art" label added, Applicants believe that no further action or comment is necessary. However, if the intended attachment included additional changes, Applicants request that a copy of the attachment be faxed to Applicants' representatives so that those additional changes may be reviewed.

Second, Applicants wish to note what they believe is a typographical error in the Statement of Reasons for Allowance, namely the statement with regard to independent claims 13 and 21 that "the first clock signal and the second clock signal are synchronized to the same clock cycle of an external source clock." What Claims 13 and 21 actually recite are that "a first clock signal that is synchronized with a first cycle of an external clock source" and "a

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second clock signal that is synchronized with a second cycle of the external clock source." Applicants believe that the above-quoted portion of the Statement of Reasons for Allowance was intended to recite "the first clock signal and the second clock signal are synchronized to respective first and second cycles of an external clock source." Otherwise, the Applicants only wish to point out that the scope and patentability of each claim is defined with respect to the recitations thereof.

It is requested that this submission be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on June 7, 2005.


Carey Gregory

Attorney Docket No. 5649-1165



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SUBMITTAL OF FORMAL DRAWINGS

Sir:

In response to the requirement for new drawings as set forth in Notice of Allowability in the above application, enclosed is one set (6 sheets) of new formal drawings. It is requested that these new drawings be substituted for the originally filed informal drawings.

Respectfully submitted,

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